

Fig.1

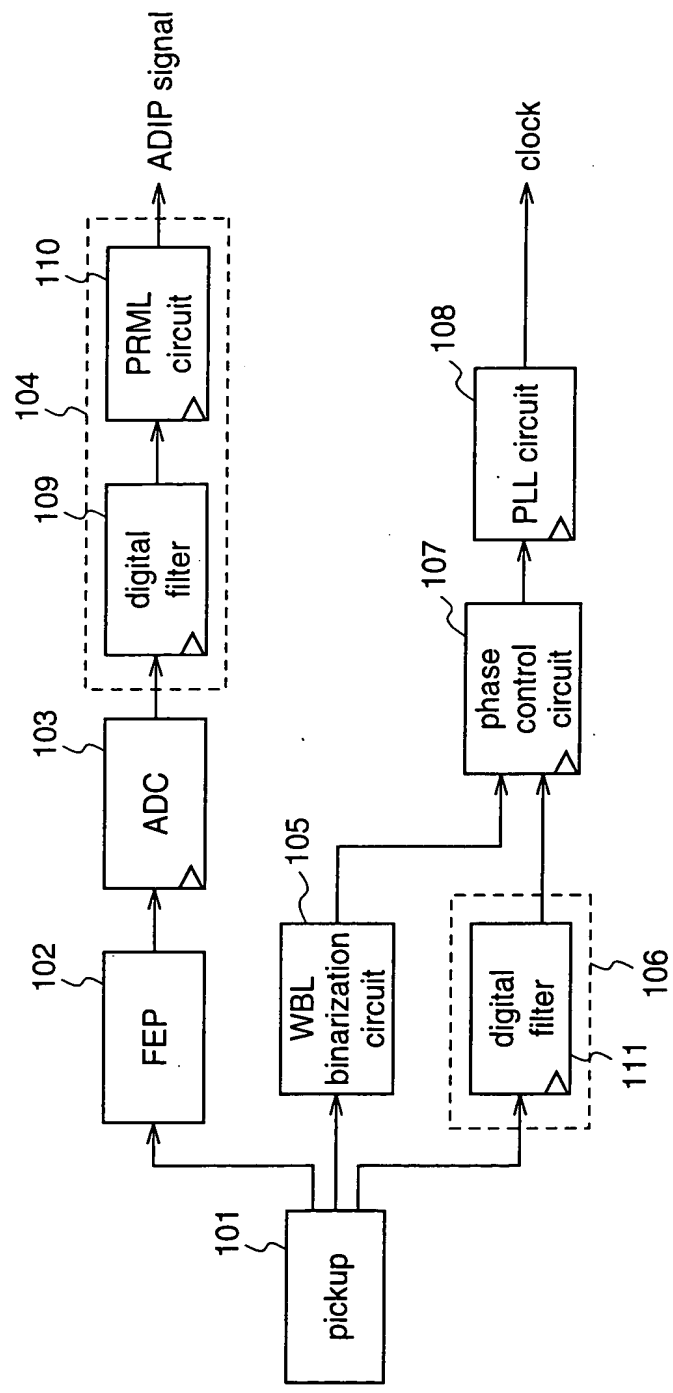


Fig.2

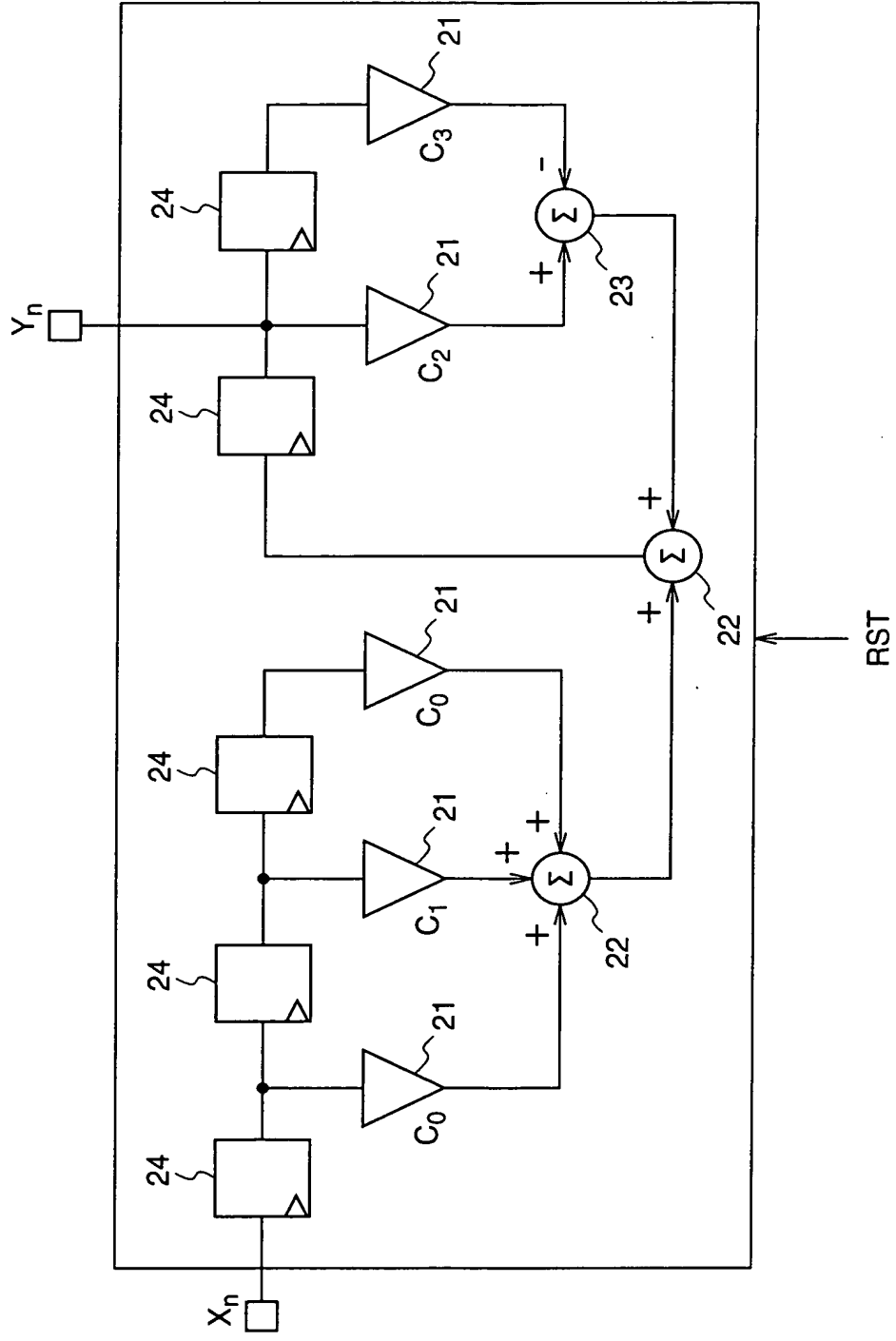
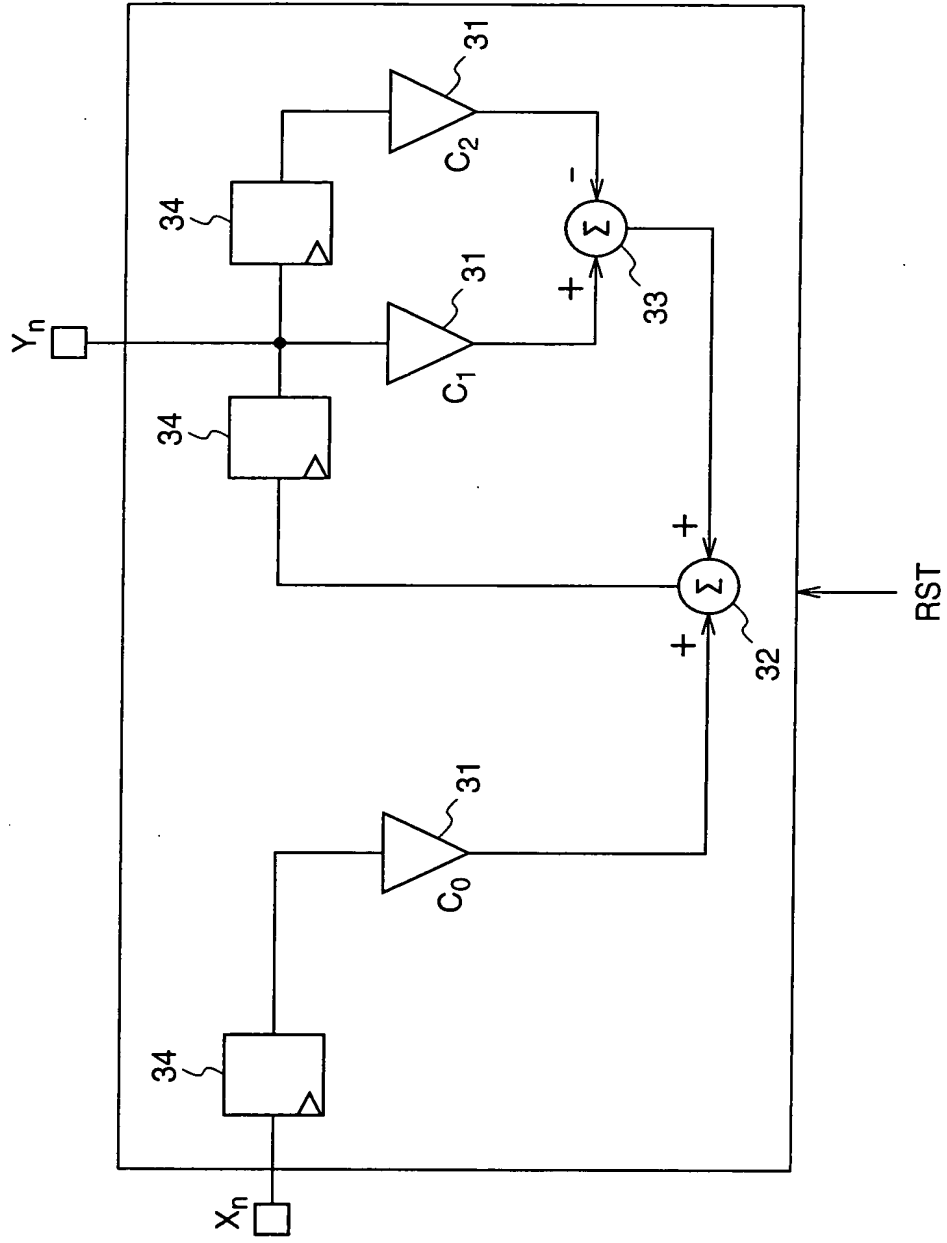
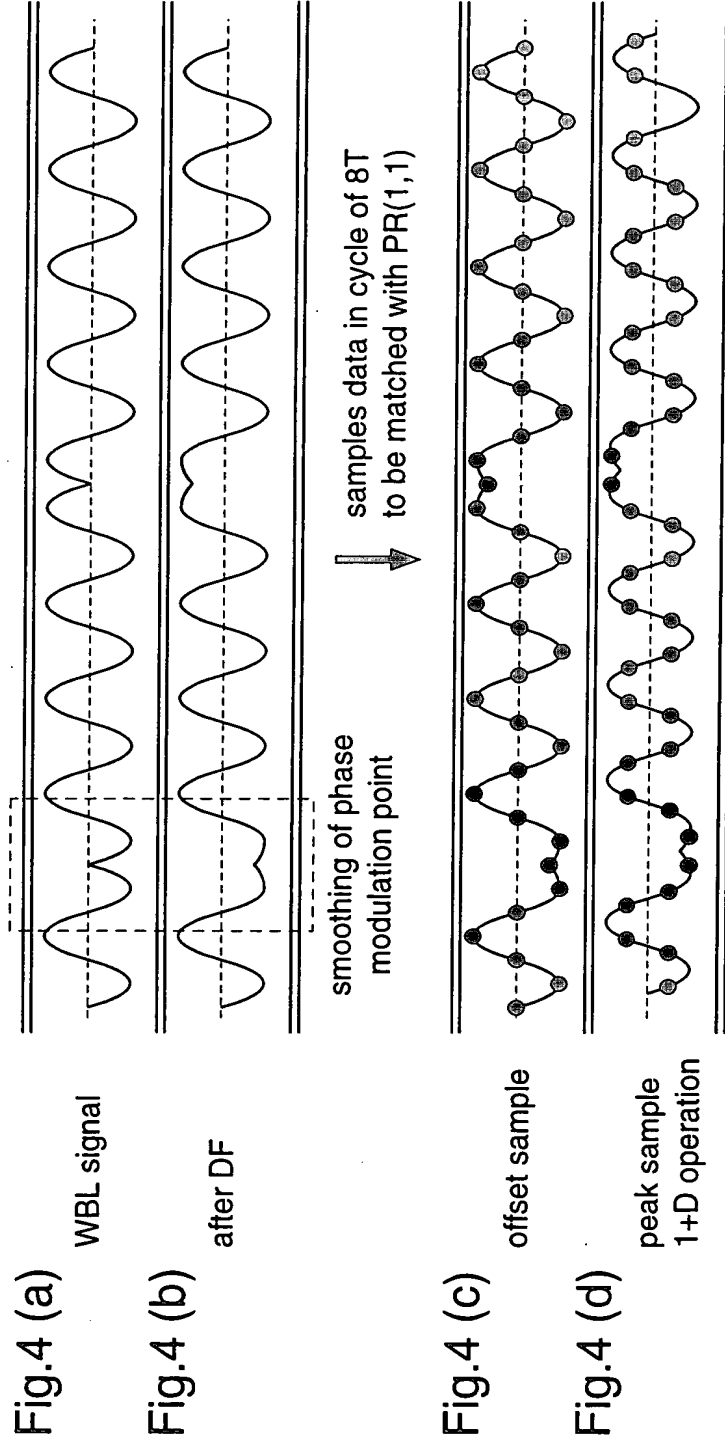


Fig.3





Codes of 2T and 4T are obtained when
sampling in cycle of 8T is carried out.

Fig.5

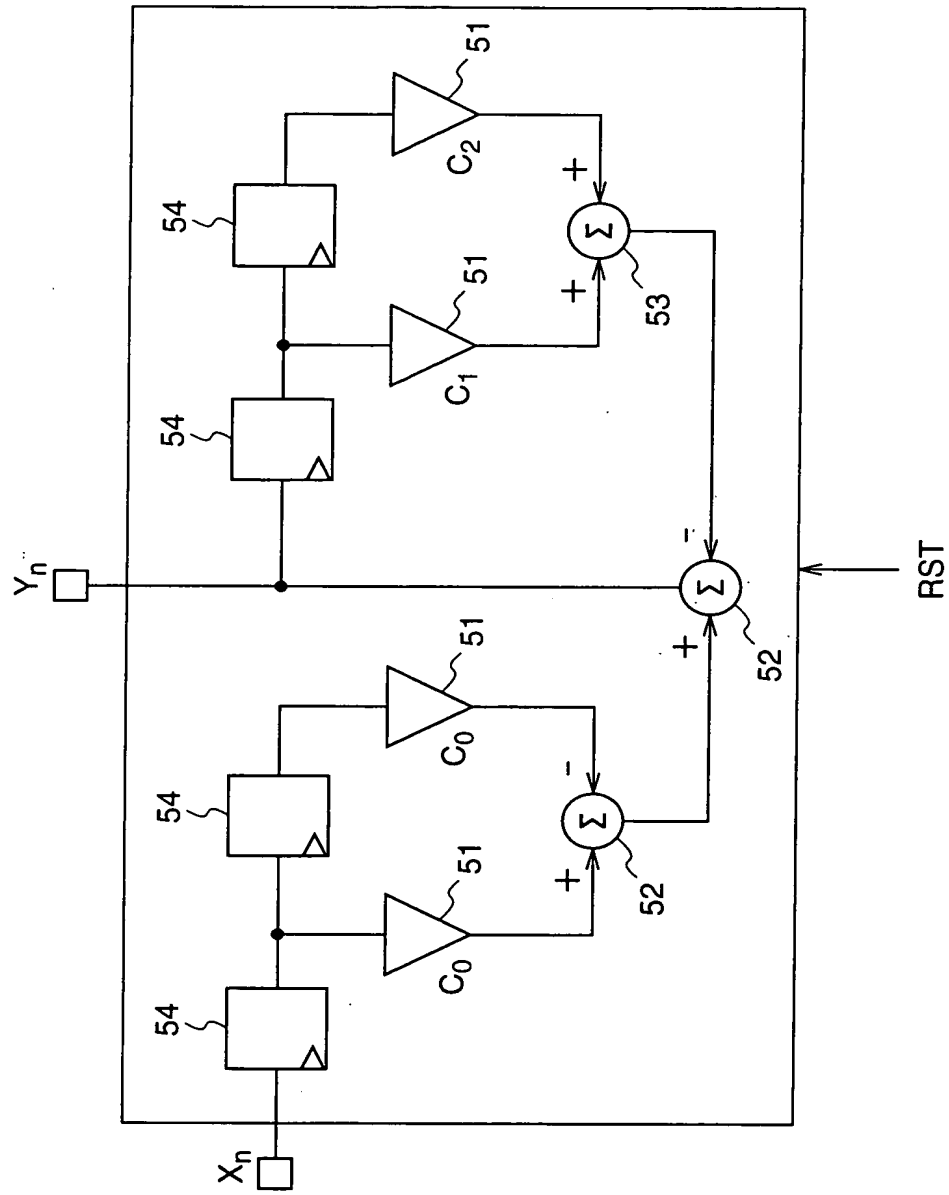


Fig.6

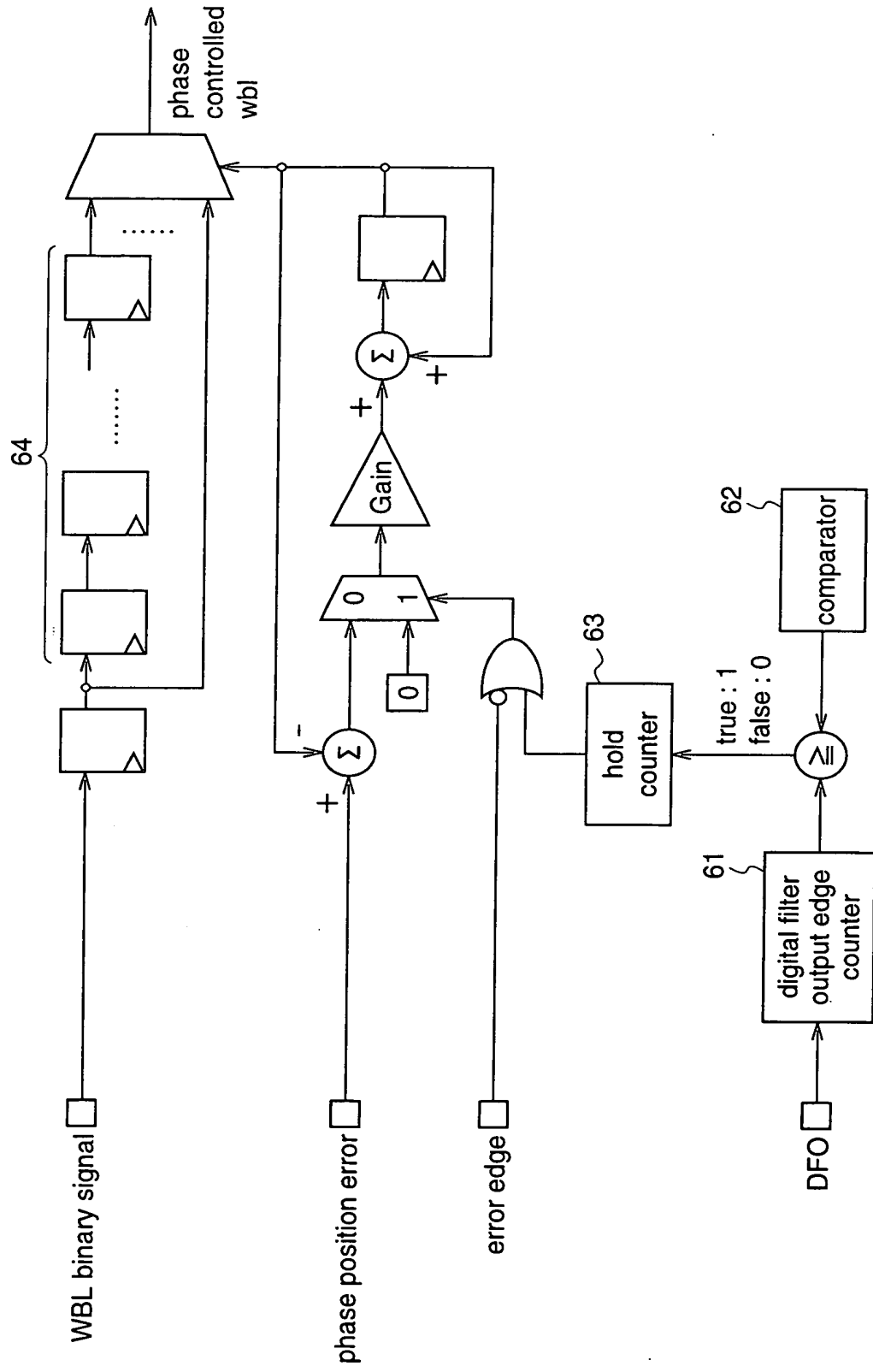


Fig.7

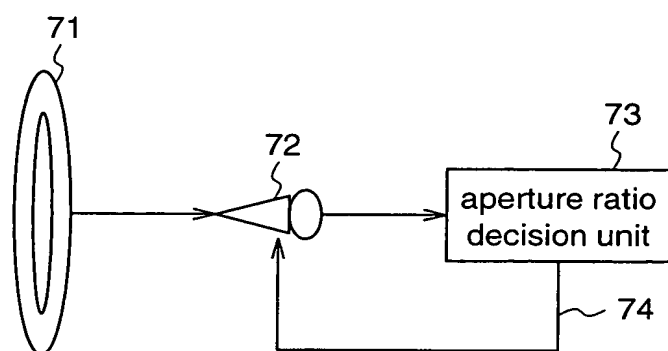


Fig.8

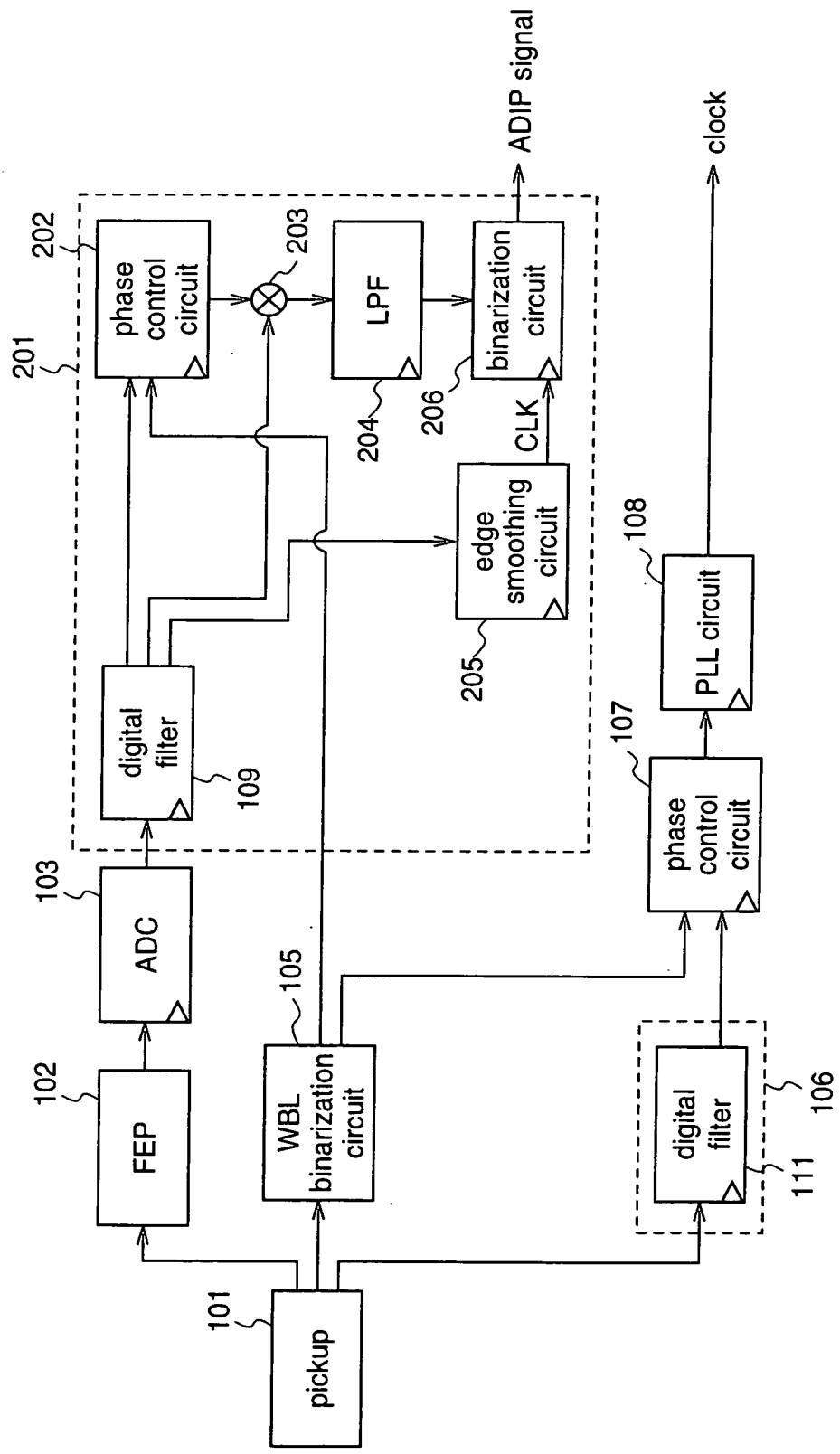


Fig.9 (a)
WBL signal

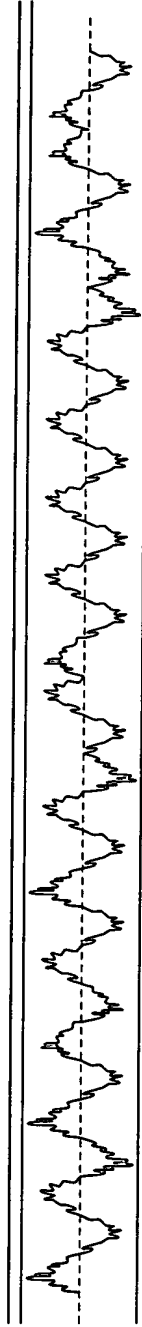


Fig.9 (b)
WBL binary
signal

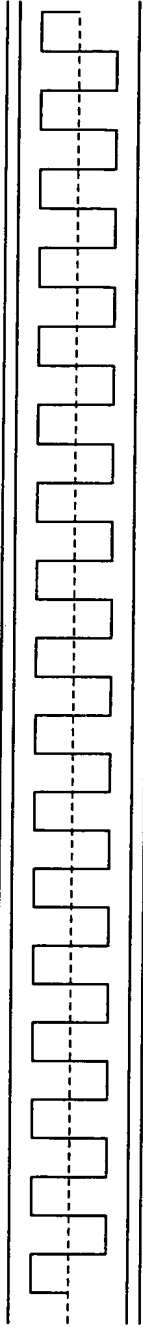


Fig.9 (c)
digital filter
output signal

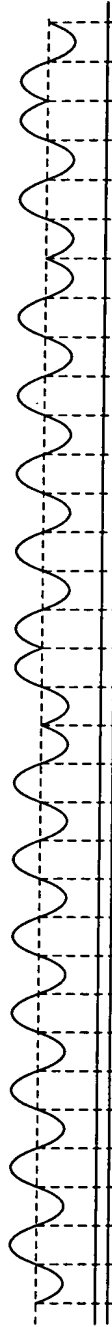


Fig.9 (d)
phase controlled
signal

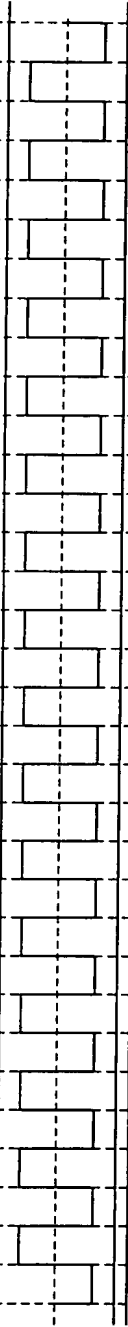


Fig.9 (e)
multiplier
output signal

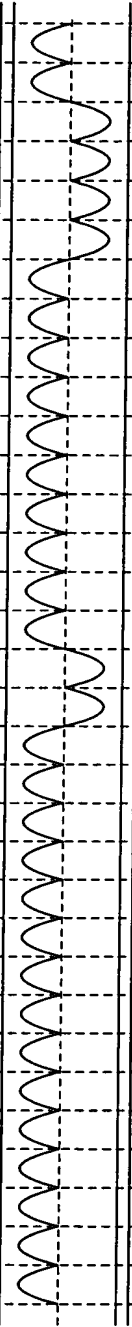


Fig.9 (f)
LPF output
signal

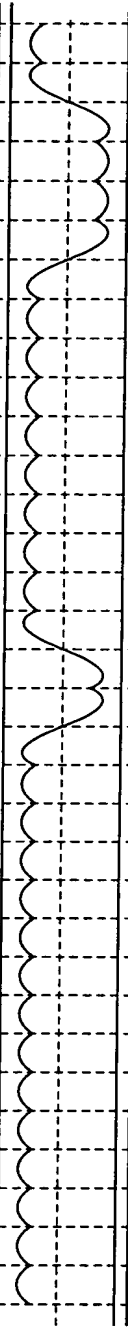


Fig.9 (g)
ADIP signal

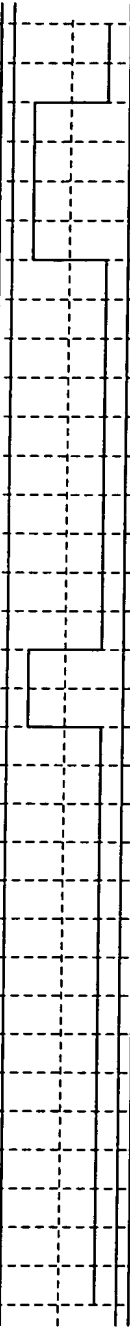


Fig.10

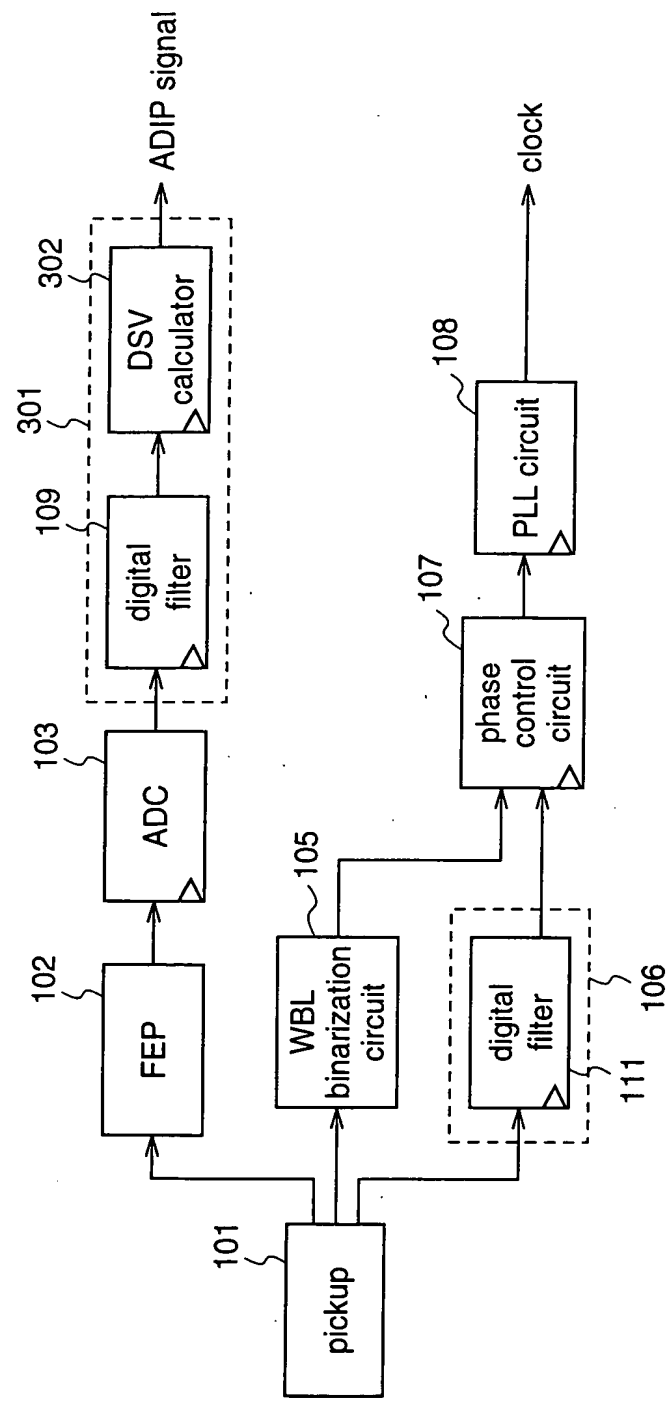


Fig.11

